

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

FIG.1A

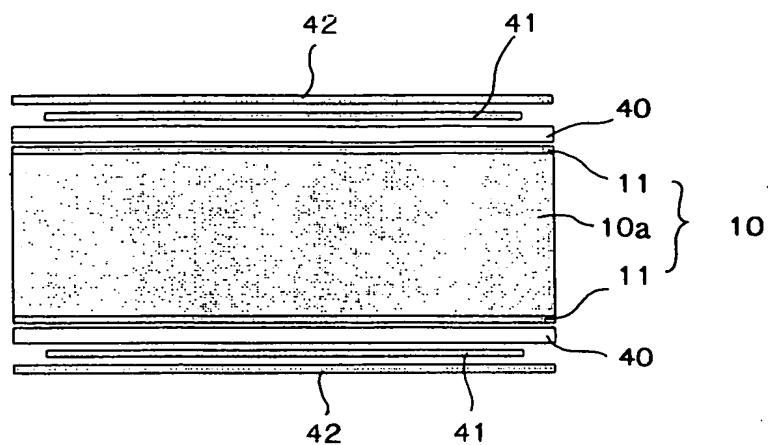


FIG.1B

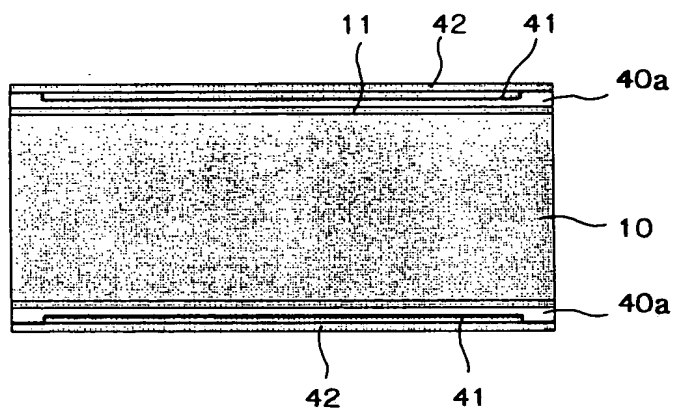
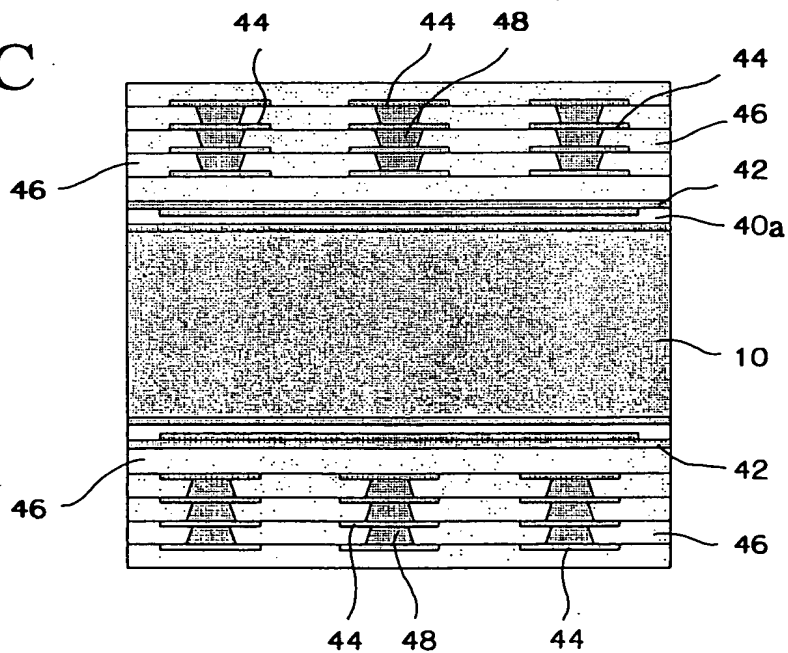
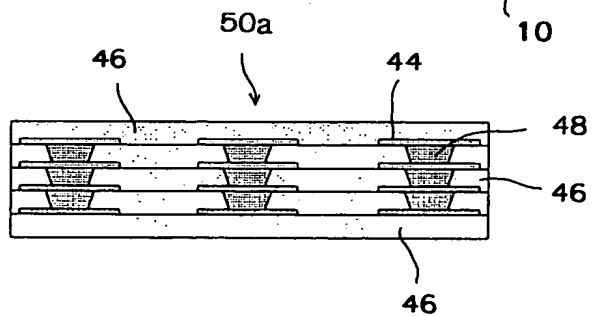


FIG.1C



[illegible]

This cross-sectional view, taken along line B-B, shows the device structure. It includes a substrate 10, a base layer 11, and a gate stack 40a. A gate electrode 41 is positioned on the left, and a gate electrode 42 is on the right. A channel region is formed between the gate electrodes. A dashed line indicates the cross-section line B-B.

A cross-sectional diagram of a multi-layered substrate assembly. It consists of three horizontal layers. The top layer is labeled 46a at its ends and 46 in the middle. The middle layer is labeled 44b at its ends and 44 in the middle. The bottom layer is labeled 46a at its ends and 44b in the middle. Between the layers are four trapezoidal voids or cavities, each labeled 50a. Arrows point from the labels to their respective parts.

FIG.4A

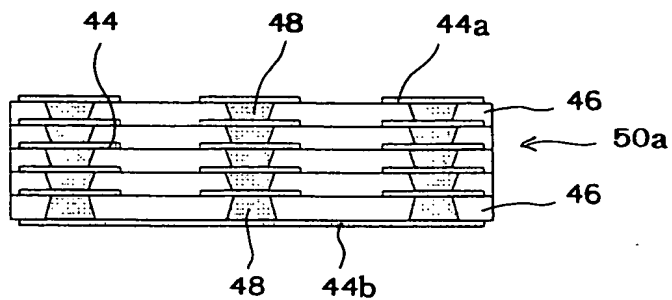


FIG.4B

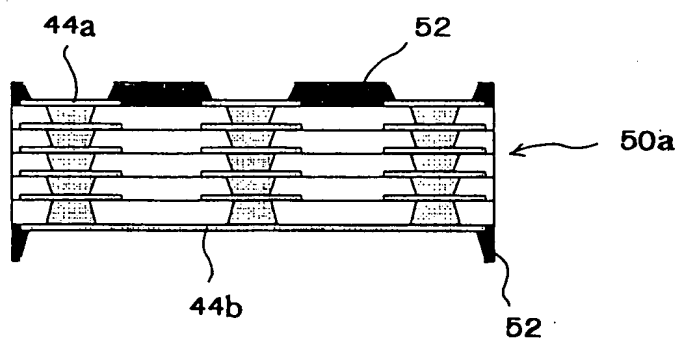


FIG.4C

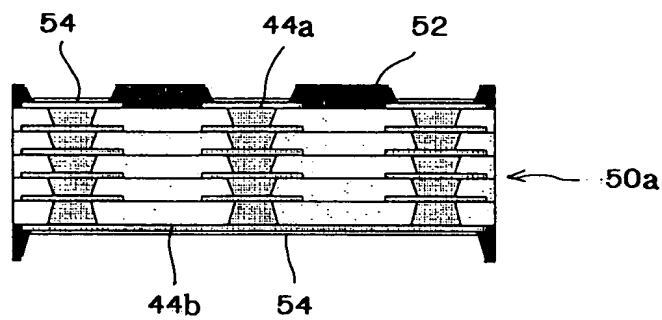


FIG.4D

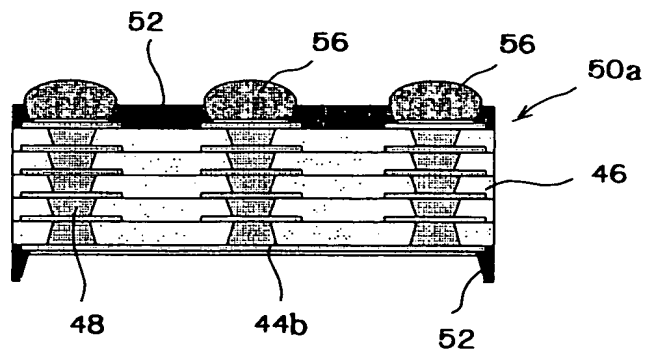


FIG.6A

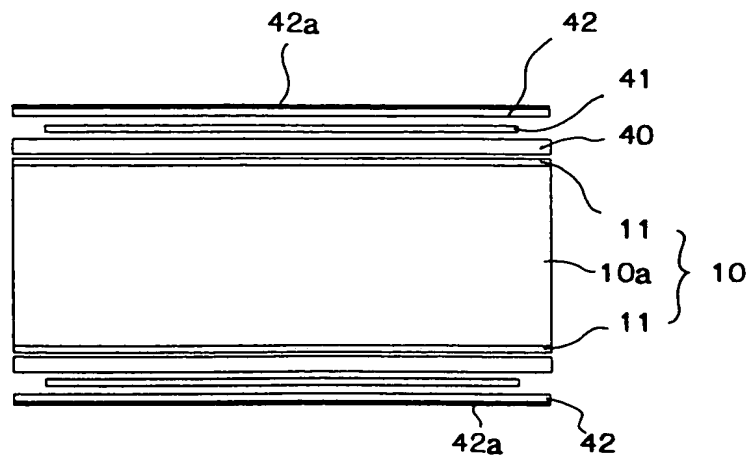


FIG.6B

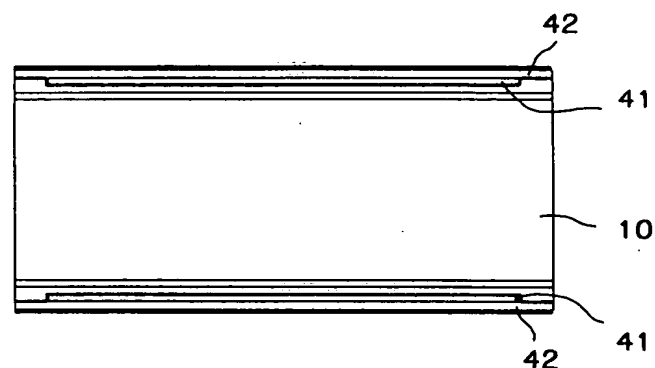


FIG.6C

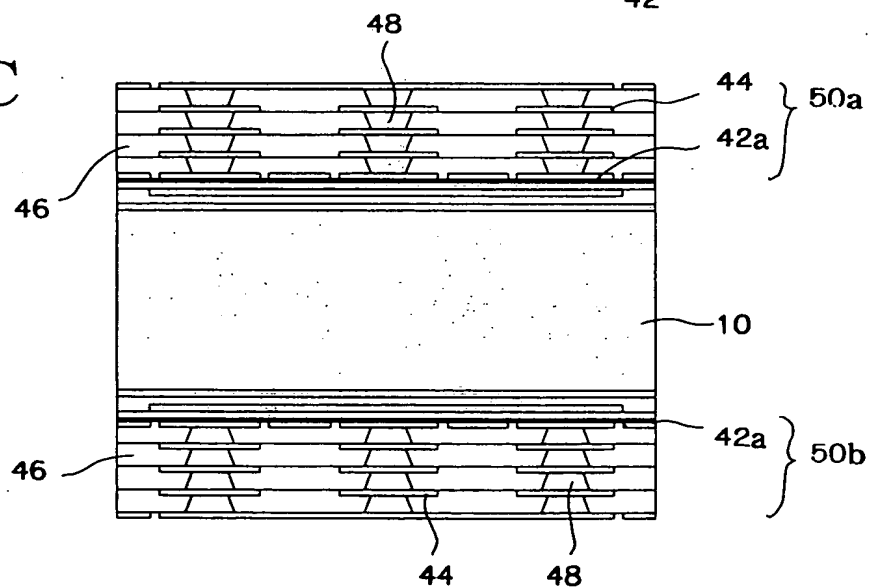


FIG.6D

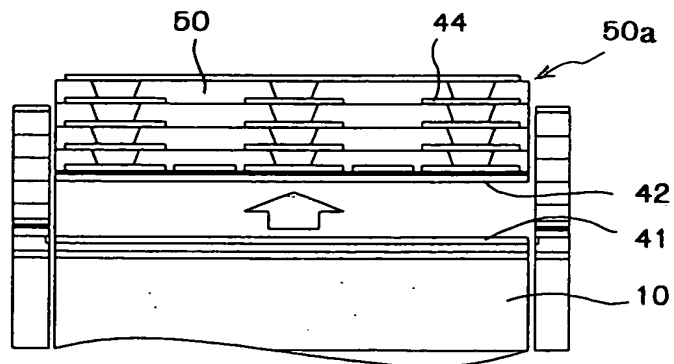


FIG.7A

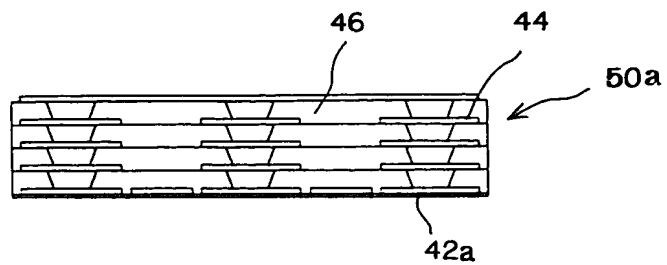


FIG.7B

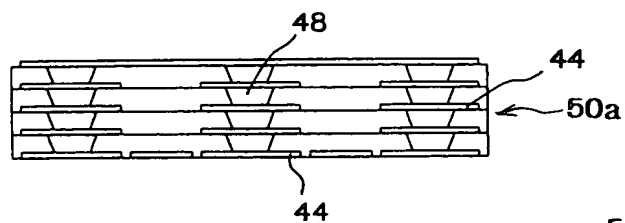


FIG.7C

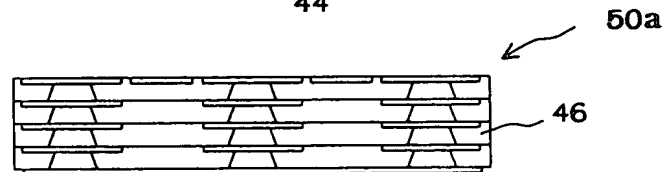


FIG.7D

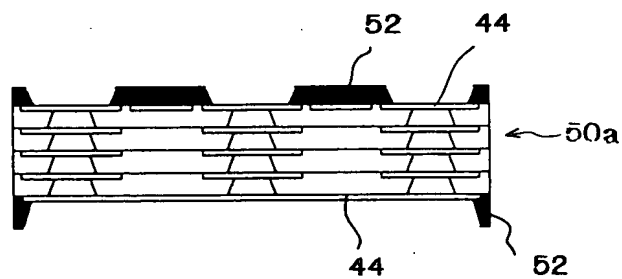


FIG.7E

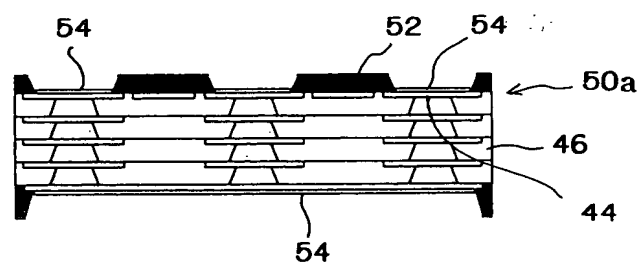


FIG.7F

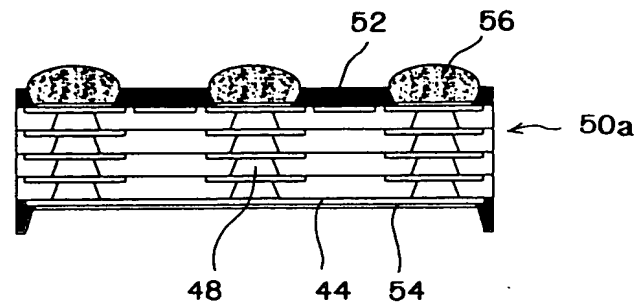


FIG.8A
PRIOR ART

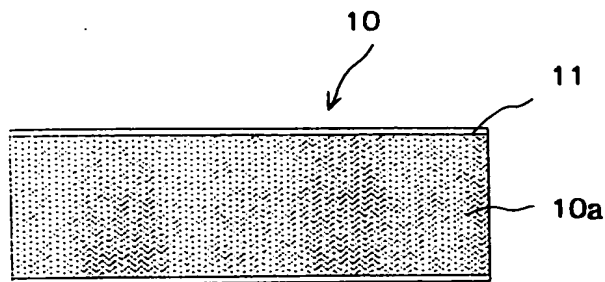


FIG.8B
PRIOR ART

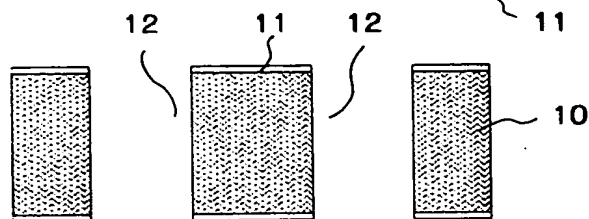


FIG.8C
PRIOR ART

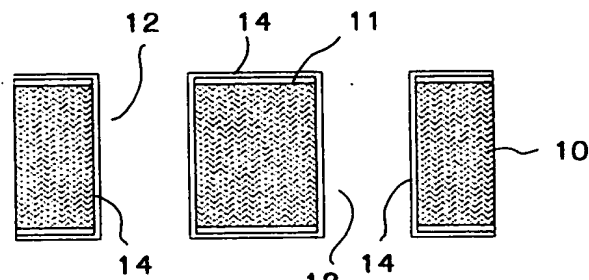


FIG.8D
PRIOR ART

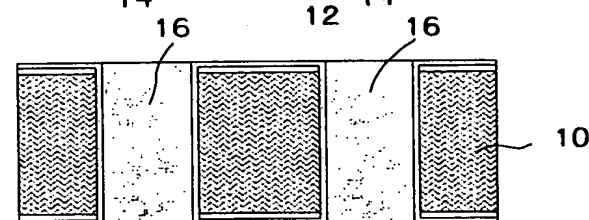


FIG.8E
PRIOR ART

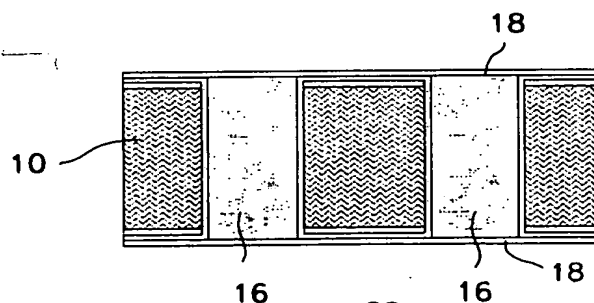


FIG.8F
PRIOR ART

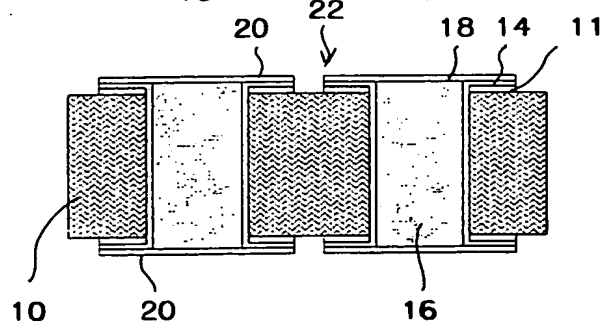


FIG.9A
PRIOR ART

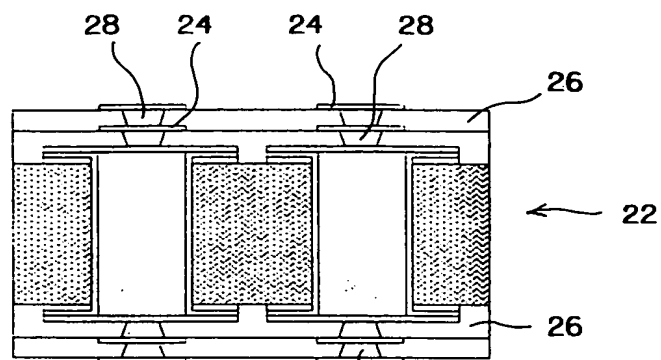


FIG.9B
PRIOR ART

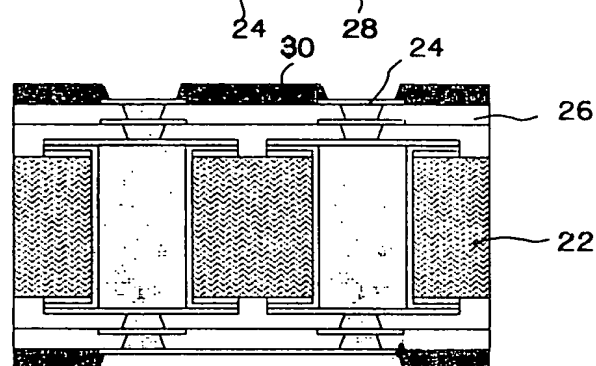


FIG.9C
PRIOR ART

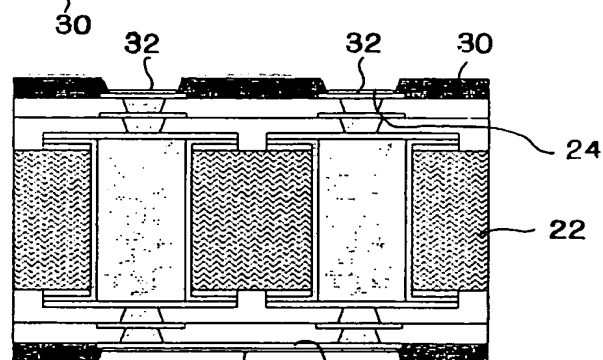


FIG.9D
PRIOR ART

